

(2) 09/940011  
(52) ((anti adj fuse antifuse))  
(0) 10/820415  
(4845) (anti adj fuse antifuse)  
(33601) (magnetoresist $\$4$  magneto adj resist $\$4$  TMR) (mtj magnetic adj tunnel adj junction)  
(3082) tunnel $\$4$  near (magnetoresist $\$4$  magneto adj resist $\$4$  TMR) (mtj magnetic adj junction)  
(1308408) ( ((ferri?magnetic ferri? adj magnetic) ((magnetic) (magnetoresist $\$4$  magneto adj resist $\$4$  TMR) (mtj magnetic adj junction)  
(87) ((anti adj fuse antifuse))  
(1839472) @ad > 20020322  
(21978117) @ad < 20020322  
(47) ((anti adj fuse antifuse))  
(1284826) magnetic  
(412) ((anti adj fuse antifuse))  
(2548) 8.ab. 8.cfm.  
(117) (((anti adj fuse antifuse))  
(495805) angstrom nm  
(38) (((anti adj fuse antifuse))  
(28) (((anti adj fuse antifuse))  
(151611) tunnel $\$4$   
(105000) (aluminum adj oxide "al.sub.20.sub.3")  
(727) tunnel $\$4$  with (aluminum adj oxide "al.sub.20.sub.3" D)  
(5287) (tunnel $\$4$  (aluminum adj oxide "al.sub.20.sub.3" D)) near8 (angstrom nm)  
(277) (tunnel $\$4$  with (aluminum adj oxide "al.sub.20.sub.3" D)  
(102835) ( (barrier conformal) near2 (diffusion layer liner))  
(6938) (anti adj ( ferri?magnetic ferri? adj magnetic magnetic) anti ferri?magnetic and ferri?magnetic)  
(1495479) cin $\$4$

Default operator:

(((anti adj fuse an(fuse))  
 )with (( ( ferr7magnetic ferr7 adj magnetic ) ((magnetic) (magnetoresist \$4 magneto adj resist \$4)) TMR (mj magnetic adj  
 tunnel adj junction))) MRAM  
 )) and @ad < 20020322)

Sept. 2004

	U	1	Inventor	Document	Issue	P	Title	Current	Current	XB	Re	Retrieval	S	C	P	3	4	5	6	7	8	9	Image	Doc.	P
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Tran, Lung T.	US 6473337	2002:1	1	Memory device having memory cells with ma	365/173	365/158				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 647333		
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Sharma, Man	US 6483734	2002:1	1	Memory device having memory cells capable	365/87	365/100				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 648373	P	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Tran, Lung T.	US 2003017	2003:0	1	Memory device array having a pair of magne	365/100					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200301		
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Eldredge, Ken	US 2003013	2003:0	1	Self-healing MRAM	365/200					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200301		
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Tran, Lung T.	US 2003008	2003:0	7	Test array and method for testing memory ar	365/201					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200300		
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Tran, Lung T.	US 6778421	2004:0	1	Memory device array having a pair of magne	365/105	365/100				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 677842		
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Fricke, Peter	US 6717215	2004:0	1	Memory structures	257/347	257/301				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 671721		
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Eldredge, Ken	US 6643195	2003:1	1	Self-healing MRAM	365/200	365/230.01				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 664319		
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Tran, Lung T.	US 6839859	2003:1	7	Test array and method for testing memory ar	365/201	365/214				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 683985		
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Miller, Paul J	US 6613979	2003:0	2	Electrical circuit suspension system	174/52	174/254				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 661397		